-	0	refresh same (pixel adj array) same (memory adj array)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/14 10:46
-	0	refresh$ same (pixel adj array) same (memory adj array)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/14 10:46
-	160	refresh$ same (pixel) same (memory)same array	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/14 10:46
-	2521	refresh$5 with (pixel or display) with memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/14 14:22
-	151	(refresh$5 with (pixel or display) with memory) same array	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/14 14:22
-	73	(refresh$5 with (pixel or display) with memory) with array	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/14 14:53
-	4	(refresh adj circuit) same memory same·pixel same array	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/14 14:55
-	0	(memory adj array same pixel adj array) same refresh$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/14 14:56
-	52	memory adj array same pixel adj array	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/14 14:56
-	19	memory adj array with pixel adj array	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/14 15:15
-	1384	refresh adj circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/14 15:16
-	8	(refresh adj circuit) same pixel same memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/14 15:20
-	2	5128658.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/14 15:25

-	2288	(circuit or control$4) same pixel same memory same array	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/14 15:26
-	85	((circuit or control$4) same pixel same memory same array) same refresh$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/14 15:43
-	2041	refresh with memory with display	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/14 15:43
-	107	(refresh with memory with display) same array	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/14 15:50
-	258	345/530,535.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/14 15:50
-	76	345/530,535.ccls. and refresh$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/14 15:51
-	38	345/530,535.ccls. and (refresh$5 with (pixel or display) with memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/14 15:51
-	0	(memory adj arrary) same (display or pixel) adj2 arrary same refreshe$4 same (cycle or tim$4)	USPAT	2003/08/21 09:58
-	0	(memory adj arrary) same (display or pixel) adj2 arrary same refreshe$4	USPAT	2003/08/21 09:58
-	829	(memory) same (display or pixel) same refreshe$4	USPAT	2003/08/21 09:58
-	115	((memory) same (display or pixel) same refreshe$4) same cycle	USPAT	2003/08/21 10:35
-	7	dram adj refresh$4 same (memory adj control$4) same (display adj controll$4)	USPAT	2003/08/21 10:44
-	51	DRAMadj refresh$4 same (memory adj control$4) same (display adj controll$4)	USPAT	2003/08/21 10:44
-	7	DRAM adj refresh$4 same (memory adj control$4) same (display adj controll$4)	USPAT	2003/08/21 10:45
-	46	(DRAM adj2 refresh$5) same (memory) same (pixel or display) same control$5	USPAT	2003/08/21 10:45
-	87619	(dynamic adj random adj access adj memory) or DRAM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 10:54
-	9898	((dynamic adj random adj access adj memory) or DRAM) same (refresh$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 10:55
-	533	(((dynamic adj random adj access adj memory) or DRAM) same (refresh$4)) same (memory) same (pixel or display)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 10:56

-	379	(((((dynamic adj random adj access adj memory) or DRAM) same (refresh$4)) same (memory) same (pixel or display)) and @ad<19990730	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 11:32
-	431	refresh near3 (control$4 or circuit) with (memory) with (pixel or display)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 11:33
-	233	(refresh near3 (control$4 or circuit) with (memory) with (pixel or display)) and @ad<19900730	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 11:35
-	20	(refresh near3 (control$4 or circuit) with (memory) with (pixel or display)) same DRAM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 12:21
-	2357	(pixel with memory) with array	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 12:21
-	24	((pixel with memory) with array) same (refresh$4 near3 (circuit or control$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 13:12
-	4	((pixel with memory) with array) with refresh$4 near3 (circuit or control$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 13:13

L Number	Hits	Search Text	DB	Time stamp
1	87619	(dynamic adj random adj access adj memory) or DRAM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 10:54
2	9898	((dynamic adj random adj access adj memory) or DRAM) same (refresh$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 10:55
3	533	(((dynamic adj random adj access adj memory) or DRAM) same (refresh$4)) same (memory) same (pixel or display)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 10:56
4	379	((((dynamic adj random adj access adj memory) or DRAM) same (refresh$4)) same (memory) same (pixel or display)) and @ad<19990730	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 11:32
5	431	refresh near3 (control$4 or circuit) with (memory) with (pixel or display)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 11:33
6	233	(refresh near3 (control$4 or circuit) with (memory) with (pixel or display)) and @ad<19900730	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 11:35
7	20	(refresh near3 (control$4 or circuit) with (memory) with (pixel or display)) same DRAM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 11:35

L Number	Hits	Search Text	DB	Time stamp
1	183	refresh adj3 (control$4 or circuit) with (pixel or display) with memory	USPAT	2004/03/16 13:30
2	13	(refresh adj3 (control$4 or circuit) with (pixel or display) with memory) same DRAM	USPAT	2004/03/16 14:10
3	3691	refresh adj3 (circuit or control$4)	USPAT	2004/03/16 14:10
4	183	(refresh adj3 (circuit or control$4)) with memory with (pixel or display)	USPAT	2004/03/16 14:11
5	169	((refresh adj3 (circuit or control$4)) with memory with (pixel or display)) and @ad<19990730	USPAT	2004/03/16 15:06
6	2574	refresh adj (circuit or control$4)	USPAT	2004/03/16 15:07
7	68	(refresh adj (circuit or control$4)) with (pixel or display) with memory	USPAT	2004/03/16 15:07
8	61	((refresh adj (circuit or control$4)) with (pixel or display) with memory) and @ad<19990730	USPAT	2004/03/16 15:07